HELLENIC REPUBLIC
MINISTRY OF DEVELOPMENT
GENERAL SECRETARIAT FOR RESEARCH AND INNOVATION

Ministry of Digital Governance, Hellenic Republic
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Date: 2023.09.15 14:46:11 EEST
Reason:
Location: Athens

FOUNDATION FOR RESEARCH AND TECHNOLOGY – HELLAS
INSTITUTE OF ELECTRONIC STRUCTURE AND LASER

One (1) PhD candidate & one (1) research assistant positions

in AGAMI_EURIGAMI project

European Innovative GaN Advanced Microwave Integration

(Call: HORIZON-EDF-2021-MATCOPM-R-2, GA 1011102983)

Funded under Directorate-General for Defence Industry and Space (DEFIS)

Funded by
the European Union

Ref. 119381

Heraklion 15/9/2023

The Institute of Electronic Structure and Laser of the Foundation for research and Technology Hellas (IESL-FORTH), in the framework of the project POWERFLEX, (P.I. G. Konstantinidis, Call: HORIZON-EDF-2021-MATCOPM-RS-2, GA ID 1011102565), funded under DEFIS, is seeking to recruit one research assistant and one PhD candidate.

PhD candidate

Electrical characterization (DC, noise and RF) and modelling of nano-scale devices

Required qualifications

- Bachelor's degree (B.Sc.) from a School of Electrical and Computer Engineering or Department of Physics (25%)
- PhD candidate in a School of Electrical and Computer Engineering with relative subject (20%)
- Technical skills: Electrical characterization (DC, noise and RF) and modelling of nano-scale MOS devices. (20%)
- Proven experience in equipment used for electrical characterization and device modelling software. (25%)
- Effective Communication & Presentation Skills, Excellent knowledge of English language (10%)

Nikolaou Plastira 100
Vassilika Vouton
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Tel. +30 2810-391300-2

Research Assistant
Characterization and modeling of compound semiconductor devices

Required qualifications

- BSc from a School of Electrical or Electronic and Computer Engineering (15%)
- MSc from a School of Electrical or Electronic and Computer Engineering (25%)
- Extensive experience in DC, CV, RF and flicker noise measurements of semiconductor devices and also in measurement automation using programming languages (ex. PEL, Python) (20%)
- Experience in commercial/open-source semiconductor modelling and measurement software (preferably in Keysight ICCAP) (10%)
- Extensive experience in compact model parameters extraction and its integration in design kits for commercially available simulators (ex. Cadence Spectre). (20%)
- Experienced user of Verilog-A programming language. Ability to develop or modify compact model for semiconductor devices. (10%)

Location: IESL-FORTH, Heraklion Crete GREECE
Start Date (earliest): December 1, 2023
Project Duration: 13 Months with possibility of extension according to the needs of the project

Application Submission
Interested candidates who meet the aforementioned requirements are kindly asked to submit their applications, no later than **September 25, 2023, 23:59 local Greece time** to the address (hr@iesl.forth.gr), with cc to Dr George Konstantinidis (aek@physics.uoc.gr).

In order to be considered, the application must include:

- Application Form (please download file from the job announcement webpage https://www.iesl.forth.gr/en/jobs-bids/jobs/job-positions)
- Detailed curriculum vitae (CV) of the candidate
- Scanned Copies of academic titles
- Certificate for enrollment in PhD program (for the PhD candidate position)

Any application received after the deadline will not be considered for the selection

Contact
For information and questions regarding the application and selection procedure, candidates are asked to contact the secretariat (hr@iesl.forth.gr), tel. +30 2810-391301.
For information and questions about the advertised position and the research activity of the group or the institute, candidates are asked to contact Dr George Konstantinidis (aek@physics.uoc.gr), tel. +30 2810-394103.

Selection Announcement
The result of the selection will be announced on the website of IESL-FORTH.
Candidates have the right to appeal the selection decision, by addressing their written objection to the IESL secretariat within five (5) days since the results announcement on the web. They also have the right to access (a) the files of the candidates as well as (b) the table of candidates' scores (ranking of candidates

results). All the above information related to the selection procedure will be available at the secretariat of IESL-FORTH in line with the Hellenic Data Protection Authority.

GDPR

FORTH is compliant with all legal procedures for the processing of personal data as defined by the **Regulation EU/2016/679 on the protection of natural persons with regard to the processing of personal data**. FORTH processes the personal data and relevant supporting documents that you have submitted to us. Processing of that data is carried out exclusively for the needs and purposes of this specific call. Such data shall not be transmitted to or communicated to any third party unless required by law. FORTH retains the above data up to the announcement of the final results of the call, unless further process and reservation is required by law or for purposes of exercise, enforcement, prosecution of certain one's legitimate legal rights' as defined in the Regulation EU/2016/679 and/or in national law. We inform you that under the Regulation EU/2016/679 you have the rights to be informed about your personal data, access to, rectification and erasure, restrictions of process and objection to as provided by applicable regulation and national laws. We acknowledge also to you, that you have the right to file a complaint to the national Data Protection Authority. For any further information regarding exercise of your personal data protection rights, you may contact the Data Protection Officer at FORTH at dpo@admin.forth.gr. You have the right to withdraw your application and consent for the processing of your personal data at any time. We inform you that, in this case, FORTH shall destroy such documents and/or supporting documents submitted and shall delete the related personal data.